IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 200301721-2

IN RE APPLICATION OF: U.S. Patent No. 6,845,472

USPTO CONFIRMATION CODE: 5915

APPLICATION NO.: 09/769,956

FILED: Jan. 25, 2001

EXAMINER: Dieu-Minh Le

GROUP ART UNIT: 2114

FOR: MEMORY SUB-SYSTEM ERROR CLEANSING

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number 09/769,956, now Patent Number 6,845,472.

A certificate of correction under 35 USC 254 is respectfully requested in the above-identified patent.

☒ All errors were the fault of the USPTO, no fee required. In the event that a further fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge **$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

☐ The errors were the fault of both the applicant and USPTO and, accordingly, please charge **$100.00** to our Deposit Account No. 082025. In the event that a further fee is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application are as follows:

In column 3, line 32, delete "Pentium HI Xeon" and insert - - Pentium II Xeon - -, therefor.
(Specification filed on Jan. 25, 2001, page 7, line 16)

In column 4, line 11, after "interface" delete "121." and insert - - 21. - -, therefor.
(Specification filed on Jan. 25, 2001, page 9, line 3)

In column 6, line 7, after "cartridge" delete "44a" and insert - - 42a - -, therefor.
(Specification filed on Jan. 25, 2001, page 13, line 2)

In column 6, line 20, after "cartridge" delete "44a" and insert - - 42a - -, therefor.
(Specification filed on Jan. 25, 2001, page 13, line 10)

In column 11, line 4, delete "ran" and insert - - run - -, therefor.
(Specification filed on Jan. 25, 2001, page 23, line 2)

In column 14, line 30, in Claim 45, after "comprises" insert - - a - -.
(Amendment to claims filed on Jun. 29, 2004, page 10, in Claim 45, line 2)

In column 16, line 16, in Claim 73, delete "REAR" and insert - - READ - -, therefor.
(Amendment to claims filed on Jun. 29, 2004, page 15, in Claim 73, line 2)
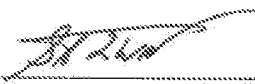
In column 16, lines 48-49, in Claim 78, delete "to a system" and insert - - to a memory sub-system - -, therefor.
(Amendment to claims filed on Jun. 29, 2004, page 16, in Claim 78, lines 1-2)

The requested corrections are attached on Form PTO 1050.

Respectfully Submitted

_____
DATE

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 6,845,472

APPLICATION NO. : 09/769,956

ISSUE DATE   : Jan. 18, 2005

INVENTOR(S)   : William J. Walker, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 3, line 32, delete "Pentium HI Xeon" and insert - - Pentium II Xeon - -, therefor.

In column 4, line 11, after "interface" delete "121." and insert - - 21. - -, therefor.

In column 6, line 7, after "cartridge" delete "44a" and insert - - 42a - -, therefor.

In column 6, line 20, after "cartridge" delete "44a" and insert - - 42a - -, therefor.

In column 11, line 4, delete "ran" and insert - - run - -, therefor.

In column 14, line 30, in Claim 45, after "comprises" insert - - a - -.

In column 16, line 16, in Claim 73, delete "REAR" and insert - - READ - -, therefor.

In column 16, lines 48-49, in Claim 78, delete "to a system" and insert - - to a memory sub-system - -, therefor.

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